

2N5460
2N5461
2N5462

**SILICON
P-CHANNEL JFETS**



TO-92 CASE



www.centrasemi.com

The CENTRAL SEMICONDUCTOR 2N5460, 2N5461, and 2N5462 are silicon P-Channel JFETs designed for low level amplifier applications.

MARKING: FULL PART NUMBER

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)

Drain-Gate Voltage

Reverse Gate-Source Voltage

Continuous Gate Current

Power Dissipation

Operating and Storage Junction Temperature

SYMBOL

V_{DG}

V_{GSR}

I_G

P_D

T_J, T_{stg}

40

40

10

310

-65 to +150

UNITS

V

V

mA

mW

$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS: ($T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	2N5460		2N5461		2N5462		UNITS
		MIN	MAX	MIN	MAX	MIN	MAX	
I_{GSS}	$V_{GS}=20\text{V}$	-	5.0	-	5.0	-	5.0	nA
I_{GSS}	$V_{GS}=20\text{V}, T_A=100^\circ\text{C}$	-	1.0	-	1.0	-	1.0	μA
I_{DSS}	$V_{DS}=15\text{V}, f=1.0\text{kHz}$	1.0	5.0	2.0	9.0	4.0	16	mA
BV_{GSS}	$I_G=10\mu\text{A}$	40	-	40	-	40	-	V
V_{GS}	$V_{DS}=15\text{V}, I_D=0.1\text{mA}$	0.5	4.0	-	-	-	-	V
V_{GS}	$V_{DS}=15\text{V}, I_D=0.2\text{mA}$	-	-	0.8	4.5	-	-	V
V_{GS}	$V_{DS}=15\text{V}, I_D=0.4\text{mA}$	-	-	-	-	1.5	6.0	V
$V_{GS}(\text{OFF})$	$V_{DS}=15\text{V}, I_D=1.0\mu\text{A}$	0.75	6.0	1.0	7.5	1.8	9.0	V
$ y_{fs} $	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{kHz}$	1.0K	4.0K	1.5K	5.0K	2.0K	6.0K	μS
$ y_{os} $	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{kHz}$	-	75	-	75	-	75	μS
C_{rss}	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{MHz}$	-	2.0	-	2.0	-	2.0	pF
C_{iss}	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{MHz}$	-	7.0	-	7.0	-	7.0	pF
e_N	$V_{DS}=15\text{V}, V_{GS}=0, f=100\text{Hz}, \text{BW}=1.0\text{Hz}$	-	115	-	115	-	115	$\text{nV}/\sqrt{\text{Hz}}$
NF	$V_{DS}=15\text{V}, V_{GS}=0, f=100\text{Hz}, R_G=1.0\text{M}\Omega, \text{BW}=1.0\text{Hz}$	-	2.5	-	2.5	-	2.5	dB

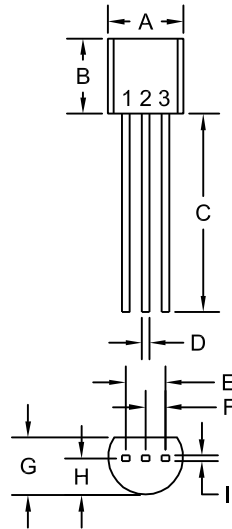
R1 (25-January 2016)

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TO-92 CASE - MECHANICAL OUTLINE



R1

SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A (DIA)	0.175	0.205	4.45	5.21
B	0.170	0.210	4.32	5.33
C	0.500	-	12.70	-
D	0.016	0.022	0.41	0.56
E	0.100		2.54	
F	0.050		1.27	
G	0.125	0.165	3.18	4.19
H	0.080	0.105	2.03	2.67
I	0.015		0.38	

TO-92 (REV: R1)

LEAD CODE:

- 1) Drain
- 2) Source
- 3) Gate

MARKING:
FULL PART NUMBER

R1 (25-January 2016)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix " TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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